

Notice of References Cited	Application/Control No. 09/751,194	Applicant(s)/Patent Under Reexamination CATHEY ET AL.	
	Examiner Tri H. Phan	Art Unit 2661	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,917,821 A	06-1999	Gobuyan et al.	370/392
	B	US-6,157,955 A	12-2000	Narad et al.	709/228
	C	US-6,628,653 B1	09-2003	Salim, Jamal Hadi	370/389
	D	US-6,804,240 B1	10-2004	Shirakawa et al.	370/392
	E	US-6,778,534 B1	08-2004	Tal et al.	370/392
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Brown et al., 'An IRAM-Based Architecture for a Single-Chip ATM Switch', TR-07-97, Center for Research in Computing Technology, Harvard University, Cambridge, Massachusetts, pp 1-32.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.